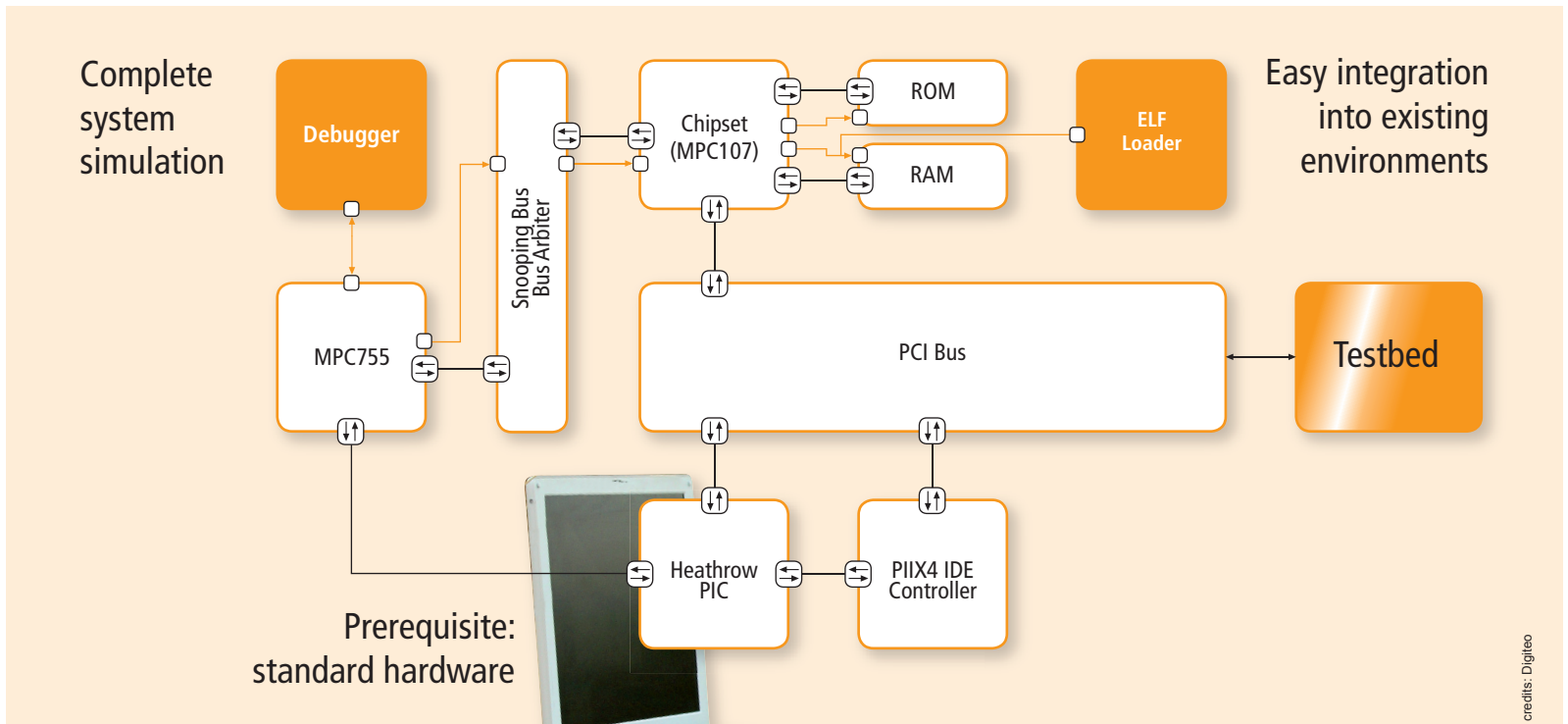


Virtual platforms: accelerated system design



W 'double v' - Siret 404 209 181 00019 - Photo credits: Digiteo

Project highlights

- 1. Integration within one unique simulation environment**
 - Covers the complete development cycle,
 - Based upon the SystemC industrial standard,
 - Allows extensive reuse of existing simulators and their components.
- 2. Innovative methods to enhance the tool's capabilities such as:**
 - Overall simulation speed,
 - System design coverage.
- 3. Complete and interoperable model libraries**

Industry targets: System integrators and all system design companies writing software on purposely built hardware platforms.

Such companies often need simulation capabilities that are not currently met by commercial products due to the **inherent hardware complexity**.

UNISIM, offering a **unified simulation environment**, provides an appealing solution due to its openness and commitment to open standards.

The project is focusing on how to optimise current standards in order to specify/implement hardware simulators. A vast library will be provided allowing to implement those methods and to apply them in real applications.

Contacts: Fabrice.Derepas@cea.fr ■ Olivier.Temam@inria.fr

